

# Meshreen MS516x ZigBee Module MS516x-Mxx series







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## 1. Introduction

The MS5168-001-Myy family is a range of ultra-low power, high performance surface mount modules targeted at IEEE 802.15.4, JenNet-IP, ZigBee Light Link, ZigBee Smart Energy and RF4CE networking applications, enabling users to realize products with minimum time to market and at the lowest cost. They remove the need for expensive and lengthy development of custom RF board designs and test suites. The modules use NXP's MS5168 wireless microcontroller to provide a comprehensive solution with large memory, high CPU and radio performance and all RF components included. All that is required to develop and manufacture wireless control or sensing products is to connect a power supply and peripherals such as switches, actuators and sensors, considerably simplifying product development.

#### 1.1 Variants

Module	Chip	Power	Antenna	Dimension(mm)	Compatible	FCC ID
MS5168-M00	JN5168-001	Standard	PCB Antenna	30x16x2.8	JN5168-001-M00	n/a
MS5168-M03	JN5168-001	Standard	u-FL connector	21x16x2.8	JN5168-001-M03	2AC2E-68M03
MS5168-M04	JN5168-001	High	u-FL connector	30x16x2.8	JN5168-001-M06	2AC2E-68M04
MS5168-M05	JN5168-001	High	PCB Antenna	30x16x2.8	N/A	2AC2E-68M04
MS5169-M03	JN5169-001	Middle	u-FL Connector	21x16x2.8	N/A	2AC2E-69M03

Table 1 Meshreen module variants



# 2. Specification

There are differences between modules, the parameters are defined here.

VDD=3.3V @ +25℃

Typical DC Characteristics			Notes	
	M00/03	M04/05	MS5169-M03	
Deep sleep current	100nA	100nA	100nA	
Sleep Current	0.70uA	0.70μΑ	0.73μΑ	With active sleep timer
Radio Transmit current	15.3mA	150mA	23.3mA	CPU + Protocol + radio transmitting
Radio receive current	17mA	22mA	15mA	CPU in doze + radio receiving
Centre frequency accuracy	+/-10ppm	+/-10ppm	+/-10ppm	Additional +/-15ppm allowance for temperature and ageing
Typical RF Characteristics				Notes
Receive sensitivity	-95dBm	-100dBm	-95dBm	Nominal for 1% PER, as per 802.15.4 section 6.5.3.3 (Note 1)
Maximum Transmit power	+2.5dBm	+18dBm	+9dBm	
Maximum input signal	10dBm	+5dBm	+10dBm	For 1% PER, measured as sensitivity
RSSI range (dBm)	-95 ∼ -10dBm	-105 ~ -20	-95~-10dBm	
RF Port impedance – SMA/uFl connector	50 ohm	50 ohm	50 ohm	2.4 - 2.5GHz
Rx Spurious Emissions	-61dBm	-69dBm	-70dBm	Measured conducted into 50 ohms
Tx Spurious Emissions	-40dBm	-49dBm	65dBm	Measured conducted into 50 ohms
VSWR (max)	2:1	2:1	2:1	2.4 - 2.5GHz
Peripherals				Notes
Master SPI port	3 selects	3 selects		250kHz - 16MHz
Slave SPI port	√	V		250kHz - 8MHz
Two UARTs	$\sqrt{}$	$\sqrt{}$		16550 compatible
Two-wire serial I/F (compatible with SMbus & I <sup>2</sup> C)	√	$\sqrt{}$		Up to 400kHz
5 x PWM (4 x timer, 1x timer/counter)	$\sqrt{}$	$\sqrt{}$		16MHz clock
'wo programmable Sleep Timers $\sqrt{}$		32kHz clock		
Digital IO lines (multiplexed with UARTs, timers and SPI selects)	20	18		
Four channel Analogue-to-Digital converter	√	$\checkmark$		10-bit, up to 100ks/s
Programmable analogue comparators	√	$\checkmark$		Ultra low power mode for sleep
Internal temperature sensor and battery monitor	√	$\checkmark$		

Table 2 Module specification



### 2.1 Pin configurations

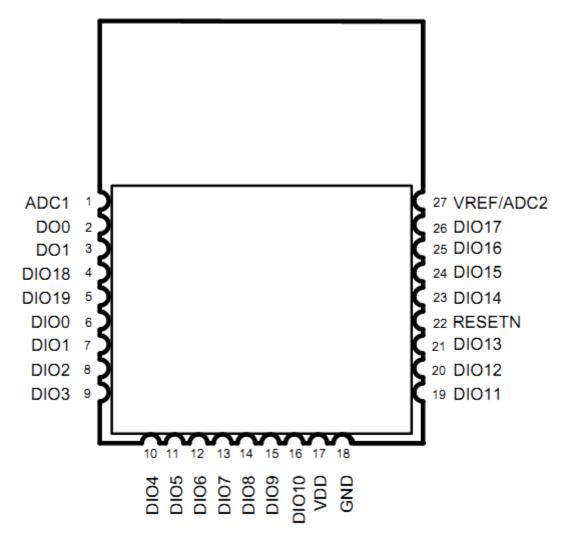


Figure 1 Pin Configuration (top view)

Note that the same basic pin configuration applies for all module designs. However, DIO3(pin9) and DIO2(pin8) are not available on the high power modules.



## 2.2 Pin Assignment

Pin							
No	Primary	Alternate	Functions			Signal Type	Description
1	ADC1					3.3V	Analogue to Digital Input
2	DO0	SPICLK			PWM2	CMOS	SPI Master Clock Output or PWM2 Output
3	DO1	SPIMISO			PWM3	CMOS	SPI Master In Slave Out Input or PWM3 Output
4	DIO18	SPIMOSI				CMOS	SPI Master Out Slave In Output
5	DIO19	SPISEL0				CMOS	SPI Select From Module – SS0 Output
6	DIO0	SPISEL1	ADC3			CMOS	DIO0, SPI Slave Select Output 1 or ADC input 3
7	DIO1	SPISEL2	ADC4	PC0		CMOS	DIO1, SPI Slave Select Output 2, ADC input 4 or Pulse Counter 0 input
8	DIO2*		RFRX	TIM0CK_GT		CMOS	DIO2, Radio Receive Control Output or Timer0 Clock/Gate Input
9	DIO3*		RFTX	TIM0CAP		CMOS	DIO3, Radio Transmit Control Output or Timer0 Capture
10	DIO4	CTS0	JTAG_TCK	TIMOOUT	PC0	CMOS	DIO4, UART 0 Clear To Send Input, JTAG CLK Input, Timer0 PWM output, or Pulse Counter 0 Input
11	DIO5	RTS0	JTAG_TMS	PWM1	PC1	CMOS	DIO5, UART 0 Request To Send Output, JTAG Mode Select Input, PWM1 Output or Pulse Counter 1 Input
12	DIO6	TXD0	JTAG_TDO	PWM2		CMOS	DIO6, UART 0 Transmit Data Output, JTAG Data Output or PWM2 Output
13	DIO7	RXD0	JTAG_TDI	PWM3		CMOS	DIO7, UART 0 Receive Data Input, JTAG Data Input or PWM 3 Output
14	DIO8	TIM0CK_GT	PC1	PWM4		CMOS	DIO8, Timer0 Clock/Gate Input, Pulse Counter1 Input or PWM 4 Output
15	DIO9	TIM0CAP	32KXTALIN	RXD1	32KIN	CMOS	DIO9, Timer0 Capture Input, 32K External Crystal Input, UART 1 Receive Data Input or 32K External Clock Input
16	DIO10	TIM0OUT	32KXTALOUT			CMOS	DIO10, Timer0 PWM Output or 32K External Crystal
17	VDD					3.3V	Supply Voltage
18	GND					0V	Digital Ground
19	DIO11	PWM1		TXD1		CMOS	DIO11, PWM1 Output or Timer1 Data Output
20	DIO12	PWM2	CTS0	JTAG_TCK	ADO or SPISMOSI	CMOS	DIO12, PWM2 Output, UART0 Clear To Send Input, JTAG CLK Input, Antenna Diversity Odd Output or SPI Slave Master Out Slave In Input
21	DIO13	PWM3	RTS0	JTAG_TMS	ADE or SPIMISO	CMOS	DIO13, PWM3 Output, UART0 Request To Send Output, JTAG Mode Select Input, Antenna Diversity Even output or SPI Slave Master In Slave Out Output
22	RESETN					CMOS	Reset input
23	DIO14	SIF_CLK	TXD0 TXD1	JTAG_TDO	SPISEL1 or SPISSEL	CMOS	DIO14, Serial Interface Clock, UART 0 Transmit Data Output, UART1 Transmit Data Output, JTAG Data Output, SPI Master Select Output 1 or SPI Slave Select Input
24	DIO15	SIF_D	RXD0 RXD1	JTAG_TDI	SPISEL2	CMOS	DIO15, Serial Interface Data or Intelligent Peripheral Data Out
25	DIO16	COMP1P	SIF_CLK	SPISMOSI		CMOS	DIO16, Comparator Positive Input, Serial Interface clock or SPI Slave Master Output Slave In Input
26	DIO17	COMP1M	PWM4	I2C DATA	SPISIMO	CMOS	DIO17, Comparator Negative Input, Serial Interface Data or SPI Slave Master In Slave Out Output
27	ADC2					3.3V	Analogue to Digital Input

Table 3 Pin Assignment

f \* These two pins are not connected for JN5168-001-M04 & JN5168 -001-M05 modules.



### 2.3 .Pin Description

All pins behave as described in the DS- MS5168 Wireless Microcontroller Datasheet, with the exception of the following:

### 2.4 Power Supplies

A single power supply pin, 3.3V VDD is provided.



## 3. Electrical Characteristics

In most cases, the Electrical Characteristics are the same for both module and chip. They are described in detail in the chip datasheet. Where there are differences, they are detailed below

#### 3.1 Maximum Ratings

Exceeding these conditions will result in damage to the device.

Parameter	Min	Max
Device supply voltage VDD	-0.3V	3.6V
All Pins	-0.3V	VDD + 0.3V
Storage temperature	-40°C	150°C

Table 4 Maximum Ratings

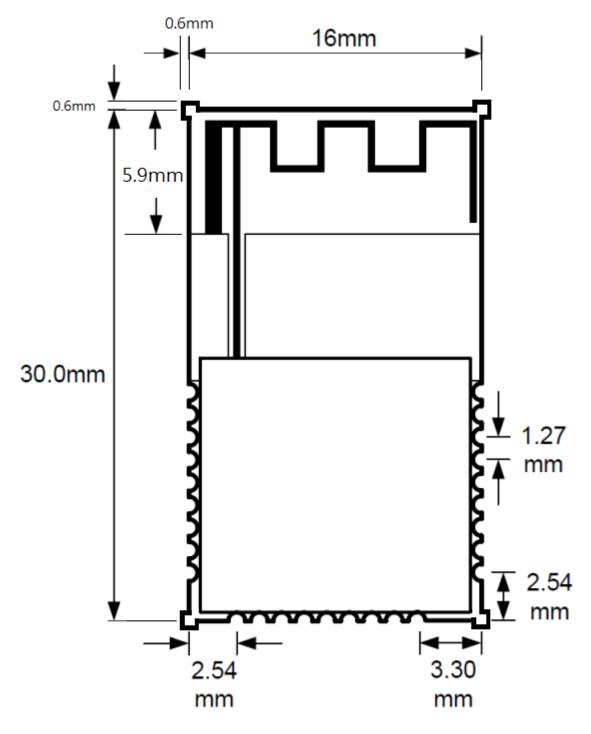
#### 3.2 Operation Conditions

Supply	Min	Max
VDD	2.0V	3.6V
Ambient temperature range	-40°C	85°C

**Table 5 Operation Conditions** 



# 4. Outline Drawing

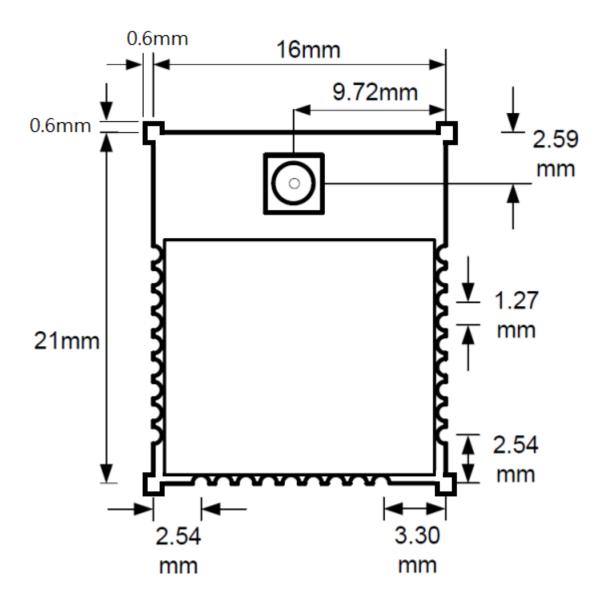


Thickness: 2.8mm

Four corner of PCB are PCB V-Cut tolerance

Figure 3 MS5168-M00 Outline Drawing



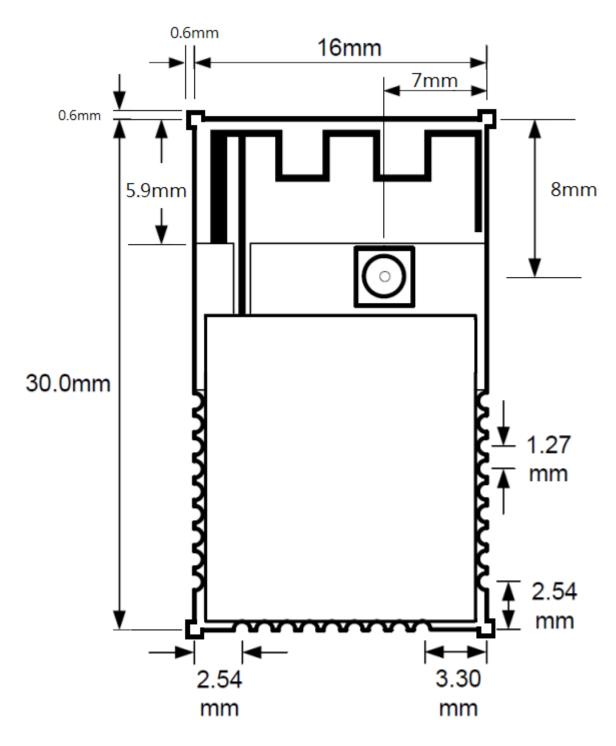


Thickness: 2.8mm

Four corner of PCB are PCB V-Cut tolerance

Figure 4 MS5168/MS5169-M03 outline drawing



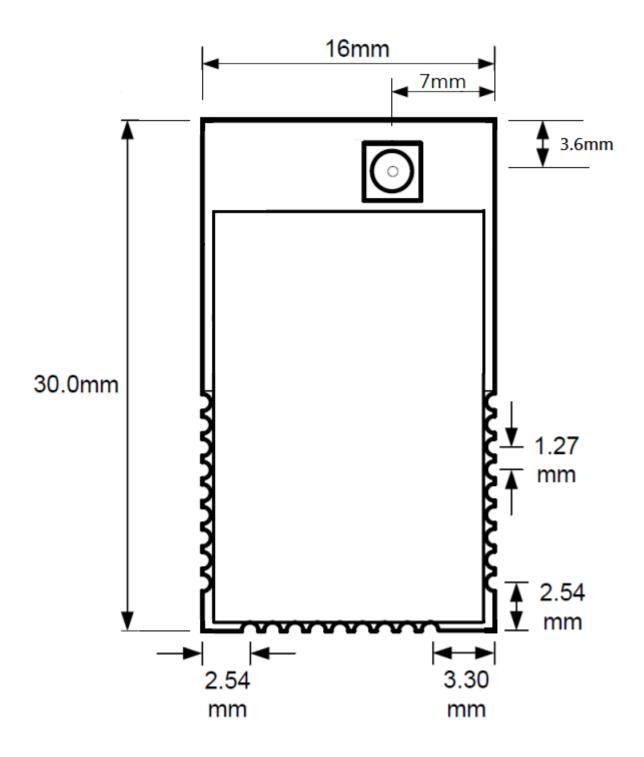


Thickness: 2.8mm

Four corner of PCB are PCB V-Cut tolerance

Figure 5 MS5168-M04/M05 Outline Drawing



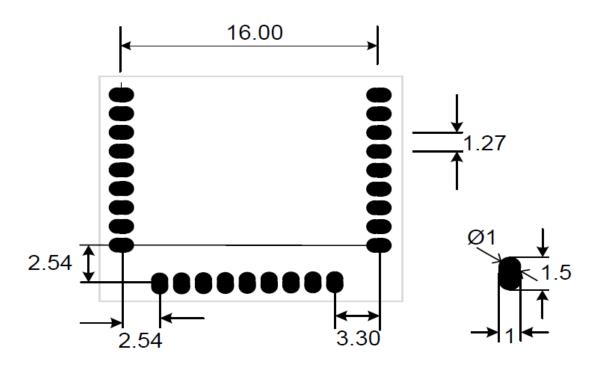


Thickness: 2.8mm

Figure 6 MS5168-M06 Outline Drawing



## 4.1 Module PCB Footprint



Note 1: All modules have the same footprint

Note 2: All Dimensions are mm

Figure 7 PCB footprint



# 5. Optimal PCB placement of the MS5168-M00/M05 Module

The JN5168-M00/M05 module features an optimized, low cost, integrated, inverted F, printed PCB antenna. The antenna has a vertically polarized near omnidirectional radiation pattern and up to 1.6 dBi of peak gain. The PCB design has been elongated in order to increase the ground plane area which increases the antenna efficiency. This allows standalone operation without any additional ground plane however care must be taken when mounting this module onto another PCB. The area around the antenna must be kept clear of conductors or other metal objects for an absolute minimum of 20 mm. This is true for all layers of the PCB and not just the top layer. Any conductive objects close to the antenna could severely disrupt the antenna pattern resulting in deep nulls and high directivity in some directions.

The diagrams below show various possible scenarios. The top 3 scenarios are correct; ground plane may be placed beneath MS5168-M00/M05 module as long as it does not protrude beyond the edge of the top layer ground plane on the module PCB.

The bottom 3 scenarios are incorrect; the left hand side example because there is ground plane underneath the antenna, the middle example because there is insufficient clearance around the antenna (it is best to have no conductors anywhere near the antenna), finally the right hand example has a battery's metal casing in the recommended keep out area.

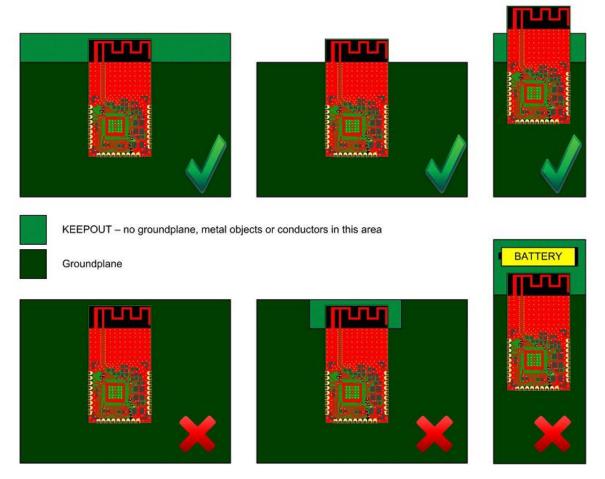


Figure 8 Optimal PCB placement

Note: The antenna radiation pattern is same as JN5168-001-M00



### 6. Reflow Profile

For reflow soldering, it is recommended to follow the reflow profile in figure 6 as a guide, as well as the paste manufacturer's guidelines on peak flow temperature, soak times, time above liquid and ramp rates.

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average Ramp-Up Rate (Ts <sub>max</sub> to Tp)	3 °C/second max.	3° C/second max.
Preheat  - Temperature Min (Ts <sub>min</sub> )  - Temperature Max (Ts <sub>max</sub> )  - Time (ts <sub>min</sub> to ts <sub>max</sub> )	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above:  - Temperature (T <sub>L</sub> )  - Time (t <sub>L</sub> )	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak/Classification Temperature (Tp)	235℃	245℃
Time within 5 °C of actual Peak Temperature (tp)	10-30 seconds	20-40 seconds
Ramp-Down Rate	6 °C/second max.	6 °C/second max.
Time 25 °C to Peak Temperature	6 minutes max.	8 minutes max.

Note 1: All temperatures refer to topside of the package, measured on the package body surface.

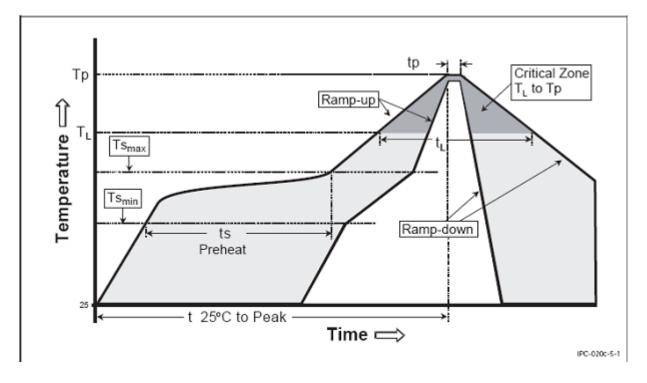


Figure 9 Module Reflow profile reference

#### 6.1 Soldering Paste and Cleaning

Meshreen does not recommend use of a solder paste that requires the module and PCB assembly to be cleaned (rinsed in water) for the following reasons:

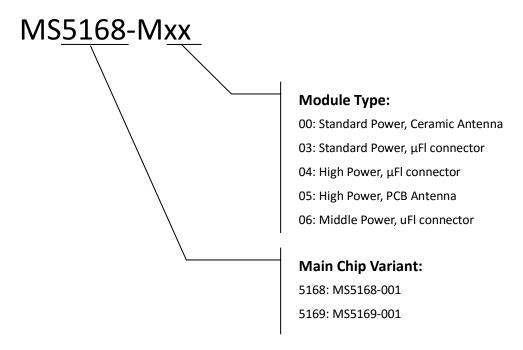
Solder flux residues and water can be trapped by the PCB, can or components and result in short circuits.

The module label could be damaged or removed.

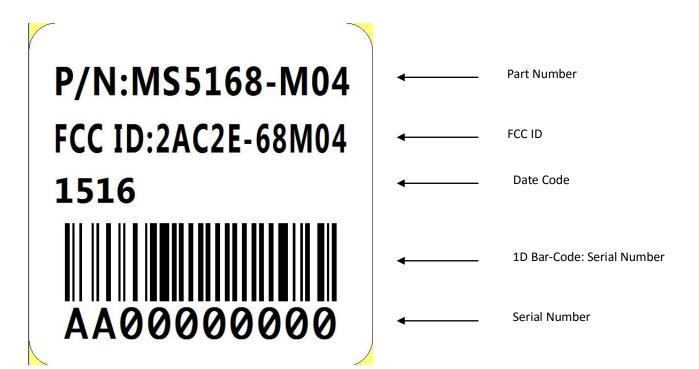
MESHREEN recommends use of a 'no clean' solder paste for all its module products.



# 7. Ordering Information



#### 7.1 Label example:





## 8. Federal Communication Commission

### Interference Statement

Any changes or modifications not expressly approved by the party responsible compliance could void your authority to operate the equipment.

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- —Reorient or relocate the receiving antenna.
- —Increase the separation between the equipment and receiver.
- —Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- —Consult the dealer or an experienced radio/TV technician for help.

#### Notice:

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference and
- (2) This device must accept any interference received, including interference that may cause undesired operation.

#### **RF Radiation Exposure Statement**

- 1. This Transmitter must not be collocated or operating in conjunction with any other antenna or transmitter.
- 2. This equipment complies with FCC RF radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with a minimum distance of 20 centimeters between the radiator and your body.

As long as the above condition is met, further transmitter testing will not be required. However, the OEM integrator is still responsible for testing their end product for any additional compliance requirements required with this module installed (for example, digital device emissions, PC peripheral requirements, etc.). This applies to the MS5168-M00 and MS5168-M03 devices; the MS5168-M04 and MS5168-M05 are mobile devices (see below)

#### High Power Module usage limitation

The high power module variants are classified as 'mobile' device pursuant with FCC § 2.1091 and must not be used at a distance of < 20 cm (8") from any people. This applies to the MS5168-M04 and MS5168-M05 module type (2AC2E-68M04).

IMPORTANT NOTE: In the event that these conditions cannot be met (for certain configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID cannot be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product



(including the transmitter) and obtaining a separate FCC authorization.

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user manual of the end product. The user manual for the end product must include the following information in a prominent location; "To comply with FCC's RF radiation exposure requirements, the antenna(s) used for this transmitter must not be co-located or operating in conjunction with any other antenna or transmitter."

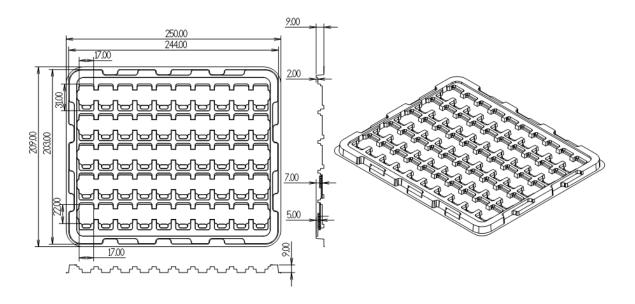
#### **FCC End Product Labeling**

The final 'end product' should be labeled in a visible area with the following: "Contains TX FCC ID: 2AC2E-68M03 or 2AC2E-68M04" to reflect the version of the module being used inside the product.



# 9. Packing

For All models are packing in one size Tray, the Tray dimension is below.



The standard packing is base on 500 pcs, use 11 trays which include cover.

All modules will packing in vacuum packing bag which include trays, humidity indicator card, desiccant.

# 10. RoHS Compliance

MS5168-Mxx modules meet the requirements of Directive 2002/95/EC of the European Parliament and of the Council on the Restriction of Hazardous Substance (RoHS).



### 11. Disclaimers

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### 12. Version Control

Version	Date	Notes
1.0	2013.11.05	1 <sup>st</sup> Issue
1.3	2014.03.21	Update PIN I/O names
1.4	2014.05.12	Add Picture, update thickness
1.5	2014.07.02	Modify module outline, Delete Label definition
1.6	2014.10.08	Add FCC warring chapter
1.7	2015.03.24	Add packing information, remove labeling information
1.8	2015.03.30	Add M06 specification
1.9	2015.05.12	Add Temperature conditions
2.0	2016.01.27	Modify FCC statement, Labeling example, Disclaimers, RoHS statement
3.0	2016.05.26	Add model MS5169-M03

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